

APS Search

(FILE 'USPAT' ENTERED AT 08:42:18 ON 16 NOV 1998)

L1 883 S FIELD### (3A) CONFIG#####
L2 518 S SYNCHRON? (P) BRIDGE# (P) (PERIPHERAL# OR DEVICE#)
L3 0 S L1 (P) L2
L4 0 S L1 (3P) L2
L5 27177 S 395/CLAS
L6 79 S L5 AND L1
L7 2 S L6/AB
L8 46 S L1/AB
L9 0 S L1 AND L2
L10 36 S (BRIDGE# OR CONTROLLER#) (P) L1
L11 84 S (PERIPHERAL# OR DEVICE#) (P) FIELD### (P) REQUEST### (P)
CO
L12 52 S L5 AND L11
L13 1 S L11/AB
L14 12 S 5325492/UREF
L15 11 S FIELD### AND L14

APS Search

1. 5,802,546, Sep. 1, 1998, Status handling for transfer of data blocks between a local side and a host side; Douglas Roderick Chisholm, et al., 711/100, 111, 156, 161 [IMAGE AVAILABLE]
2. 5,794,069, Aug. 11, 1998, Information handling system using default status conditions for transfer of data blocks; Douglas Roderick Chisholm, et al., 395/822, 200.45, 306, 309, 310, 840, 856; 711/163 [IMAGE AVAILABLE]
3. 5,787,300, Jul. 28, 1998, Method and apparatus for interprocess communications in a database environment; Joyo Wijaya, 395/800.01; 370/282, 299; 395/800.26 [IMAGE AVAILABLE]
4. 5,706,432, Jan. 6, 1998, Mechanism for receiving messages at a coupling facility; David Arlen Elko, et al., 395/200.43, 200.63, 843, 849 [IMAGE AVAILABLE]
- ✓ 5. 5,671,441, Sep. 23, 1997, Method and apparatus for automatic generation of I/O configuration descriptions; Steven Gardner Glassen, et al., 395/828, 821, 830, 836, 839, 858 [IMAGE AVAILABLE]
6. 5,617,570, Apr. 1, 1997, Server for executing client operation calls, having a dispatcher, worker tasks, dispatcher shared memory area and worker control block with a task memory for each worker task and dispatcher/worker task semaphore communication; Edward A. Russell, et al., 395/684; 364/DIG.1 [IMAGE AVAILABLE]
7. 5,606,666, Feb. 25, 1997, Method and apparatus for distributing control messages between interconnected processing elements by mapping control messages of a shared memory addressable by the receiving processing element; Carl H. Grant, et al., 395/200.46; 711/107, 202 [IMAGE AVAILABLE]
8. 5,596,726, Jan. 21, 1997, Method and system for buffering transient data using a single physical buffer; David Thielen, 395/200.64; 348/719; 395/824 [IMAGE AVAILABLE]
- ✓ 9. 5,574,862, Nov. 12, 1996, Multiprocessing system with distributed input/output management; Ronald Marianetti, II, 395/280; 364/228.1, 229, 940, DIG.1, DIG.2; 395/288, 308, 740; 711/114, 147 [IMAGE AVAILABLE]
- ✓ 10. 5,491,799, Feb. 13, 1996, Communication interface for uniform communication among hardware and software units of a computer system; Ronald K. Kreuzenztein, et al., 395/200.43; 364/246.8, 281, 285, DIG.1; 711/152 [IMAGE AVAILABLE]
- ✓ 11. 5,448,708, Sep. 5, 1995, System for asynchronously delivering enqueue and dequeue information in a pipe interface having distributed, shared memory; James P. Ward, 395/200.65, 310; 711/147 [IMAGE AVAILABLE]

APS Search

1. 5,325,492, Jun. 28, 1994, System for asynchronously delivering self-describing control elements with a pipe interface having distributed, shared memory; Francis M. Bonevento, et al., 395/309; 364/281, DIG.1; 701/121, 200 [IMAGE AVAILABLE]

US PAT NO: 5,325,492 [IMAGE AVAILABLE]
DATE FILED: Jun. 11, 1993

L13: 1 of 1

ABSTRACT:

A microprocessor system includes a processor unit, one or more subsystem adapter units, optional I/O **devices** which may attach to the adapters, and a bus interface. Memory in the processor and memory in the adapters are used by the system as a shared memory which is **configured** as a distributed First In First Out (FIFO) circular queue (a pipe). Unit to unit asynchronous communication is accomplished by placing self-describing control elements on the pipe which represent **requests**, replies, and status information. The units send and receive self-describing control elements independent of the other units which allows free. . . pipe for outbound control elements and the other pipe for inbound control elements. The control elements have standard fixed header **fields** with variable **fields** following the fixed header. The fixed header allows a common interface protocol to be used by different hardware adapters. The. . .